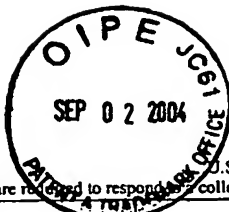


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INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>		Application Number	10/029,699
		Filing Date	12/18/2001
		First Named Inventor	Chi-Keung LUK
		Group Art Unit	2183
		Examiner Name	Unknown
Sheet 1 of 2	Attorney Docket Number	200302164-1 (1662-46800)	

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OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate) title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, page(s), volume-issued number(s), publisher, city and/or country where published.	T ²
KTN	AA	M. Franklin, "Incorporating Fault Tolerance in Superscalar Processors," Proceedings of High Performance Computing, December, 1996 (pp. 301-306).	
KTN	AB	A. Mahmood et al., "Concurrent Error Detection Using Watchdog Processors - A Survey," IEEE Trans. on Computers, Vol. 37, No. 2, February 1988 (pp. 160-174).	
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KTN	AN	M. Franklin, "A Study Of Time Redundant Fault Tolerance Techniques For Superscalar Processors," undated (9 pp.).	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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LIST OF PATENTS AND PUBLICATIONS FOR
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(Use several sheets if necessary)

ATTY. DOCKET NO.

200302164-1

APPLICATION NO.

10/029,699

CONFIRMATION NO.

3236

APPLICANT

Chi-Keung LUK et al.

FILING DATE

12/18/2001

GROUP

2183

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
KTN	1A	5,758,142	05/26/1998	S. McFarling et al.	
KTN	1B	5,933,860	08/03/1999	J. S. Emer et al.	
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	1M					
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OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, etc.)

	1Q	
	1R	
	1S	

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